Kevin Lee

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EDUCATION

VIRGINIA TECH
MS in Computer Engineering
June 2015 | Blacksburg, VA
GPA: 3.6 / 4.0

BS in Computer Engineering
Minor in Computer Science
May 2013 | Blacksburg, VA
GPA: 3.4 / 4.0

COURSEWORK

GRADUATE
Computer Architecture
Operating Systems
Electronic Design Automation
Bayesian Robotics
Test & Verification of Digital Systems

UNDERGRADUATE
Computer Systems
Computer Organization
Embedded System Design
Computer and Network Security
VLSI Circuit Design
Network Application Design
Cryptography II
Digital Design II
Digital Image Processing

SKILLS

CERTIFICATIONS
MCDST • MCSA • MCSE Security

PROGRAMMING
Platforms:
Windows • Linux • PIC • MSP430
Over 5000 lines:
C • C++ • C#
Over 1000 lines:
Assembly • Java • Python • Verilog
Familiar:
Android • Git • SVN • SQLite • VHDL

CODE REPOSITORY
svn://0x7d.com/code-repo/
Auth: Guest/8gv5f4mC2M
Repository is also accessible directly
from my website (password: SVN)

OVERVIEW

Computer Engineering; seeking opportunities throughout the entire stack in embedded systems, systems integration, or interdisciplinary robotics applications.

EXPERIENCE

MICROSOFT CORPORATION | Software Engineer
June 2015 – Present | Redmond, Washington
- Working on improving device security for the next generation Xbox console

VULNERABILITY RESEARCH LABS | Developer Intern
June 2011 – August 2011 | Reston, Virginia
- Created a bug tracker visualization webpage using Protovis, Flask, MongoDB
- Wrote DLL modules to extend functionality of existing applications
- Developed multi-threaded Windows applications and services using WinAPI

SOFTWARE ASSISTANCE AND TRIAGE (SWAT) | IT Technician
Fall 2009 – Fall 2012 | Blacksburg, Virginia
- Provides technical support for engineering students at Virginia Tech
- Designed and implemented a new backup system to speed up office workflow
- Developed applications to automate many tasks normally done by the technicians, improving productivity by an average factor of $4\times$!

RESEARCH

CONFIGURABLE COMPUTING MACHINES (CCM) LAB
Graduate Research Assistant
March 2013 – May 2015 | Blacksburg, VA
- Assisted in a VBI-NSF summer workshop to simplify accelerated genome sequencing through the integration of LabView and Convey HPC servers
- Working on exploring the impact of module shaping and placement in modular FPGA design and assembly flows. Tools were written to benchmark and improve an in-house partial reconfiguration flow capable of generating a full FPGA bitstream from pre-compiled modules in a matter of seconds

AUTONOMOUS MASTERY PROTOTYPING (AMP) LAB
Co-Founder, Lab Assistant, Mentor
March 2012 – May 2015 | Blacksburg, VA
- Worked on numerous projects to gain experience in other fields, including PCB design and assembly, high speed embedded systems, robotic applications, and mechanical designs tailored for 3D printing
- Mentored and provided guidance to other students on their own projects
- Worked with other students on the leadership team to evolve the lab

OTHER ACTIVITIES

Fall 2011 – Spring 2012	IEEE Robotics
Fall 2007 – Spring 2009	FIRST Robotics Competition